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Applicant(s): William R. Wheeler et al.

MODELING A LOGIC DESIGN

REPLACEMENT SHEET

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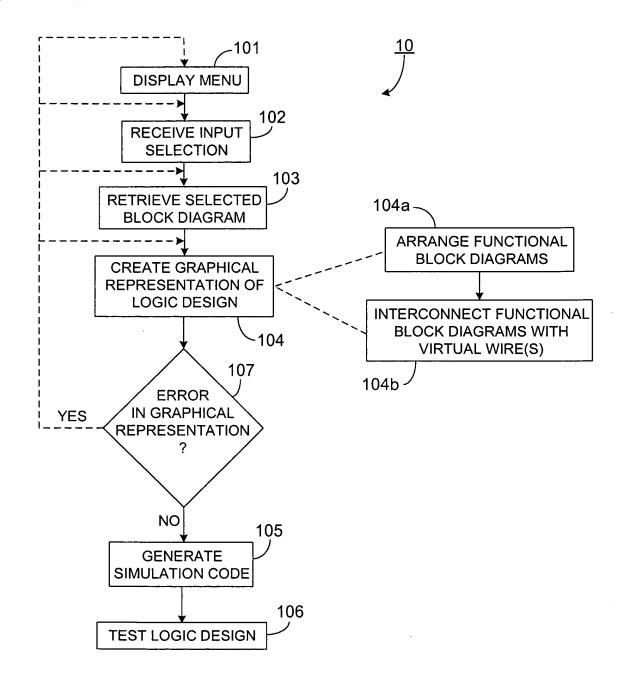
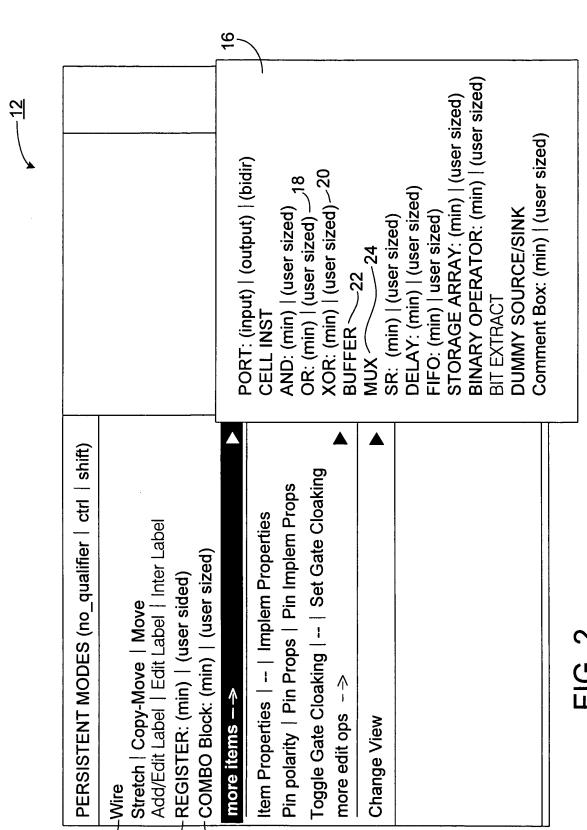


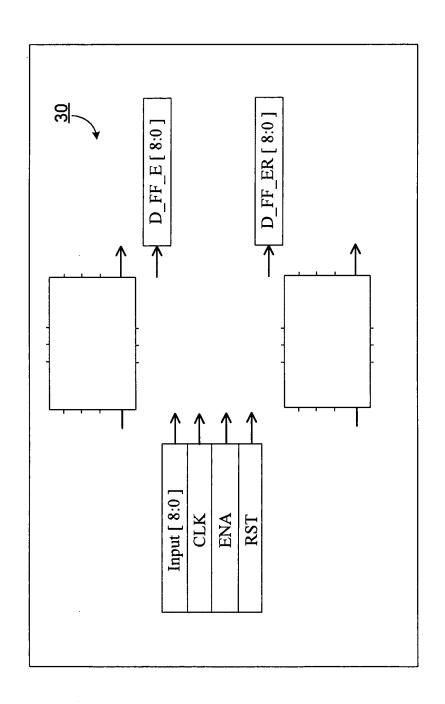
FIG. 1

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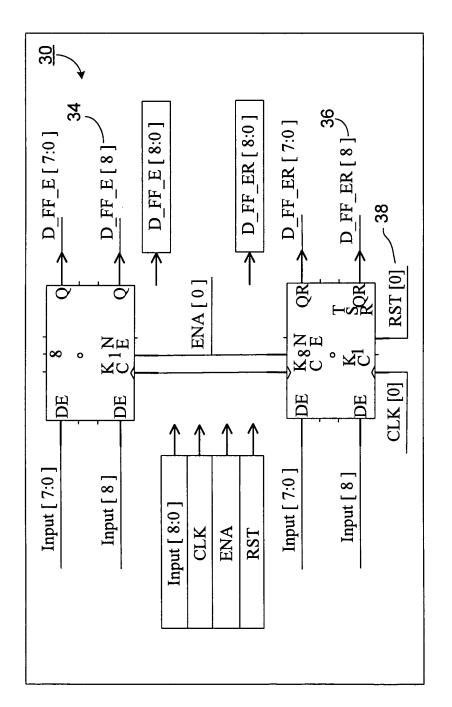


FIG. 4

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